

CLAIM LISTING

1. (Currently Amended) A system for displaying images on a single display, said system comprising:

a decoder for decoding encoded images and parameters associated with the images, thereby resulting in decoded images and decoded parameters associated with the decoded images;

a plurality of image buffers for storing the decoded images prior to display on the single display, wherein single ones of the decoded images are displayed at a time on the single display;

a plurality of parameter buffers, wherein each of the plurality of parameters buffers corresponds to a particular one of the plurality of image buffers and is for storing the decoded parameters associated with the image stored in the corresponding one of the plurality of image buffers, prior to display on the single display, wherein single ones of the decoded images are displayed at a time on the single display; and

a display engine for receiving the decoded parameters from the parameter buffers and providing the decoded images for display on the single display using the decoded parameters stored in the parameter buffers, wherein single ones of the decoded images are displayed at a time on the single display.

2. (Previously Presented) The system of claim 1, wherein the encoded images and the parameters associated with the images form portions of data packets.

3. (Original) The system of claim 2, wherein the data packets comprise headers, wherein the headers comprise the parameters.

4. (Original) The system of claim 3, wherein the headers comprise picture layer headers.

5. (Original) The system of claim 3, wherein the headers comprise sequence layer headers.

6. (Original) The system of claim 2, wherein the data packets are associated with first headers and second headers, wherein the first headers comprise a portion of the parameters, and wherein the second headers comprise another portion of the parameters.

7. (Original) The system of claim 6, wherein the first headers comprise picture layer parameters and wherein the second headers comprise sequence layer parameters.

8. (Original) The system of claim 1, wherein the encoded images comprise compressed images.

9. (Original) The system of claim 1, wherein the parameters are encoded with a variable length code, and wherein the decoder decodes the variable length code.

10. (Currently Amended) A circuit for displaying images on a display, said circuit comprising:

a decoder;

a plurality of image buffers connected to the decoder and configured to store images decoded by the decoder;

a plurality of parameter buffers connected to the decoder, wherein each of the plurality of parameters buffers corresponds to a particular one of the plurality of image buffers and is and configured to store parameters associated with the image stored in the corresponding one of the plurality of image buffers;

a display engine connected to the image buffers and the parameter buffers and configured to receive the decoded parameters from the parameter buffers and providing the decoded images for display using the decoded parameters stored in the parameter buffers, said display engine separate from the parameter buffers.

11. (Previously Presented) The circuit of claim 10, wherein the encoded images and the parameters associated with the images form portions of data packets.

12. (Original) The circuit of claim 11, wherein the data packets comprise headers, wherein the headers comprise the parameters.

13. (Original) The circuit of claim 12, wherein the headers comprise picture layer headers.

14. (Original) The circuit of claim 12, wherein the headers comprise sequence layer headers.

15. (Original) The circuit of claim 11, wherein the data packets are associated with first headers and second headers, wherein the first headers comprise a portion of the parameters, and wherein the second headers comprise another portion of the parameters.

16. (Original) The circuit of claim 15, wherein the first headers comprise picture layer parameters and wherein the second headers comprise sequence layer parameters.

17. (Cancelled)

18. (Original) The circuit of claim 10, wherein the parameters are encoded with a variable length code, and wherein the decoder decodes the variable length code.

19-26. (Cancelled).

27. (Previously Presented) The system of claim 1, wherein the decoded parameters include at least one parameters selected from a group consisting of presentation time stamp, top field first, and repeat first field.

28. (New) A system for providing images for display, said system comprising:

    a decompression engine for decompressing the images and decoding parameters, thereby resulting in decompressed images;

    a first frame buffer for storing a first one of the decompressed images;

    a first parameter buffer for storing parameters associated with the first one of the decompressed images;

    a second frame buffer for storing a second one of the decompressed images;

    a second parameter buffer for storing parameters associated with the second one of the decompressed images;

a third frame buffer for storing a third one of the decompressed images;

a third parameter buffer for storing parameters associated with the third one of the decompressed images; and

a display engine for providing the first decompressed image for display based on the parameters stored in the first parameter buffer, providing the second decompressed image for display based on the parameters stored in the second parameter buffer, and providing the third decompressed image for display based on the parameters stored in the third parameter buffer on a single display, one image at a time.

29. (New) The system of claim 28, wherein the parameters associated with the first image are decoded during decompression of the first compressed image, wherein the parameters associated with the second image are decoded during decompression of the second compressed image, and wherein the parameters associated with the third image are decoded during decompression of the third compressed image.